LMN3368AXF 30V N-Channel Enhancement Mode MOSFETs

Features

- $R_{DS(ON)}$ =6mΩ @ V_{GS}=10V
- $R_{DS(ON)}$ =9.8mΩ @ V_{GS}=4.5V
- DFN5x6-8L Package

Product Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

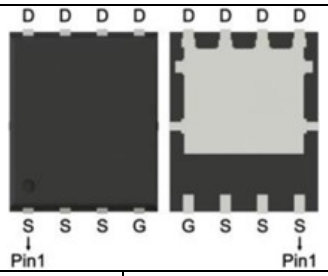
The device is well suited for high efficiency fast switching applications.

Applications

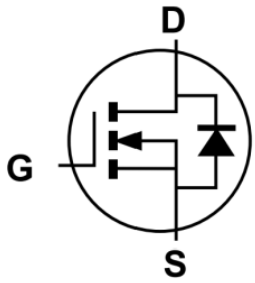
- MB / VGA / Vcore
- POL
- SMPS 2nd SR

Pin Configuration

LMN3368AXF (DFN5x6-8L)



PIN	Description
1,2,3	Source
4	Gate
5,6,7,8	Drain



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Ordering Information

Part Number	Part Marking	Package	Quantity
LMN3368AXF	3368AXF	DFN5x6-8L	3000 PCS

Marking Information

Part Marking	Package Code	Green Level:	Product Code:
3368AXF	1 is X for SOP-8	2 is F for RoHS Compliant and Halogen Free	LMN3368AXF

Absolute Maximum Ratings

(T_C=25ºC Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		±20	V
I_D	Continuous Drain Current[1]	T_A=25ºC	70	A
		T_A=100ºC	45	
I_{DM}	Pulsed Drain Current[2]		180	A
I_{AS}	Single Pulse Avalanche Current, L = 0.5mH [3]		12	A
E_{AS}	Single Pulse Avalanche Energy, L = 0.5mH [3]		72	mJ
P_D	Power Dissipation[4]	T_A=25ºC	58	W
		T_A=100ºC	22	
T_J	Operating Junction Temperature		-55 to +150	℃
T_{STG}	Storage Temperature Range		-55 to +150	℃
$R_{\theta JA}$	Thermal Resistance-Junction to Case[1]		2.1	℃/W

Note:
1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2oz copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. EAS ratings are based on low frequency and duty cycles to keep TJ=+25ºC.
4. The power dissipation is limited by 150ºC junction temperature.

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Electrical Characteristics

(T_C=25ºC Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	30			V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, ID=250uA	1.2		2.5	
I_{GSS}	Gate Leakage Current	V_{DS}=0V, V_{GS}=±20V			±100	nA
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V			1	uA
$R_{DS(on)}$	Drain-Source On-Resistance	V_{GS}=10V, I_D=15A		4.2	6	mΩ
		V_{GS}=4.5V, I_D=10A		5.6	9.8	
V_{SD}	Diode Forward Voltage	I_S=20A, V_{GS}=0V			1.2	V
Dynamic						
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=10V, I_D=15A		39		nC
Q_{gs}	Gate-Source Charge			7.6		
Q_{gd}	Gate-Drain Charge			7.2		
C_{iss}	Input Capacitance	V_{DS}=15V, V_{GS}=0V, f=1MHz		2295		pF
C_{oss}	Output Capacitance			267		
C_{rss}	Reverse Transfer Capacitance			210		
$t_{d(on)}$	Turn-On Time	V_{DD}=15V, I_D=15A, V_{GS}=10V, R_G=3.3Ω		7.8		ns
t_r				15		
$t_{d(off)}$	Turn-Off Time			37		
t_f				11		
R_g	Gate Resistance	V_{DS}=0V, V_{GS}=0V, f=1MHz		1.7		Ω

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Typical Performance Characteristics

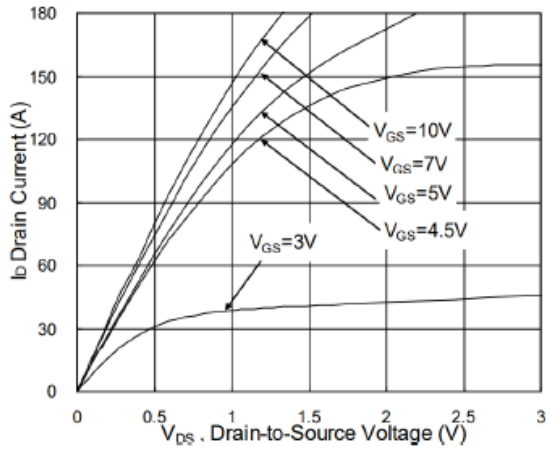

Figure 1. Typical Output Characteristics

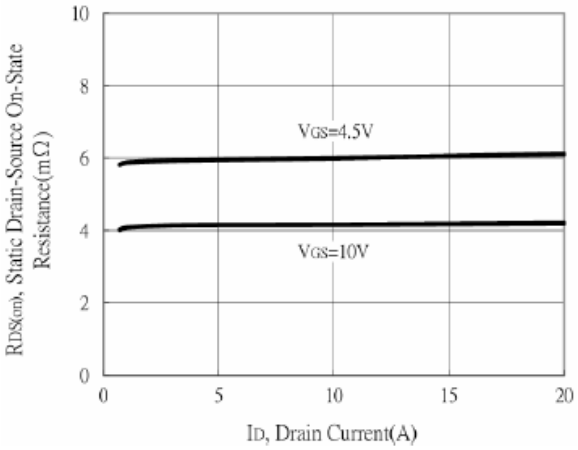

Figure 2. Drain-Source On-State resistance vs Drain Current

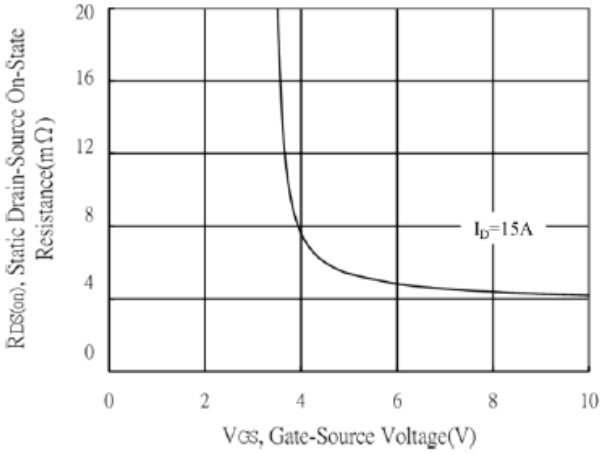

Figure 3. Drain-Source On-State Resistance vs Gate-Source Voltage

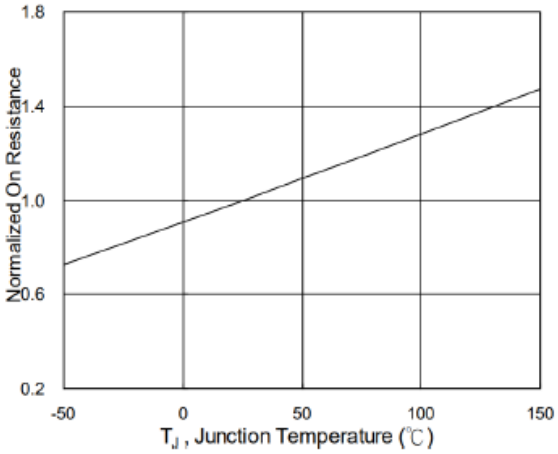

Figure 4. Drain-Source On-State Resistance vs Junction Temperature

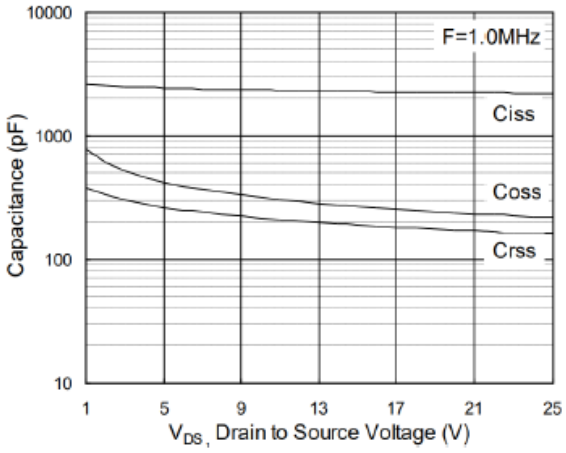

Figure 5. Capacitance vs Drain-to-Source Voltage

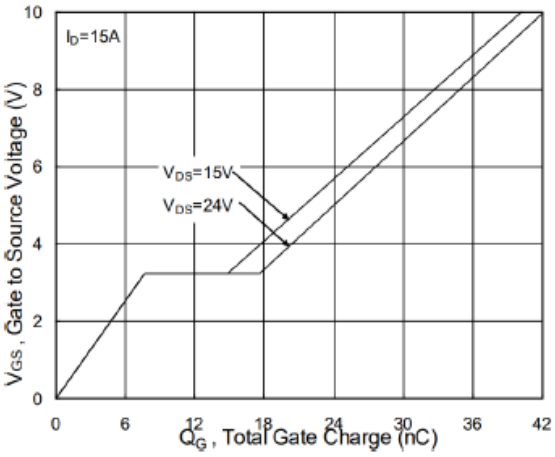

Figure 6. Gate Charge

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Typical Performance Characteristics(continue)

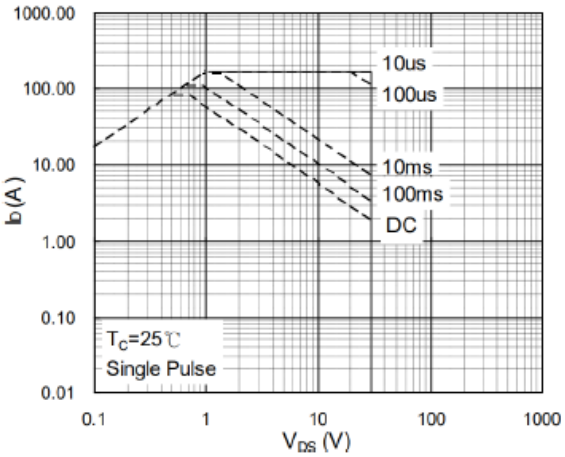

Figure 7. Maximum Safe Operating Area

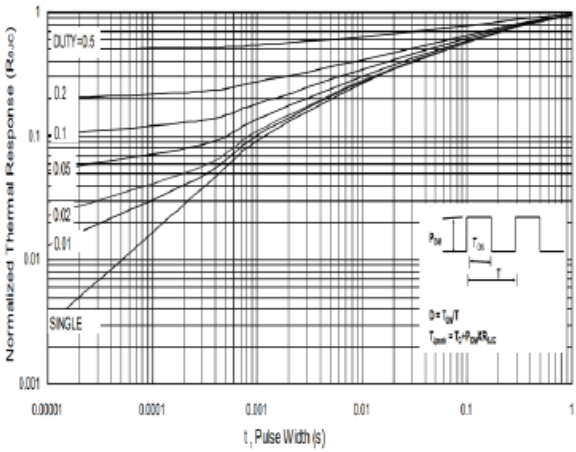

Figure 8. Normalized Transient Thermal Resistance

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Package Dimension:

DFN5x6-8L

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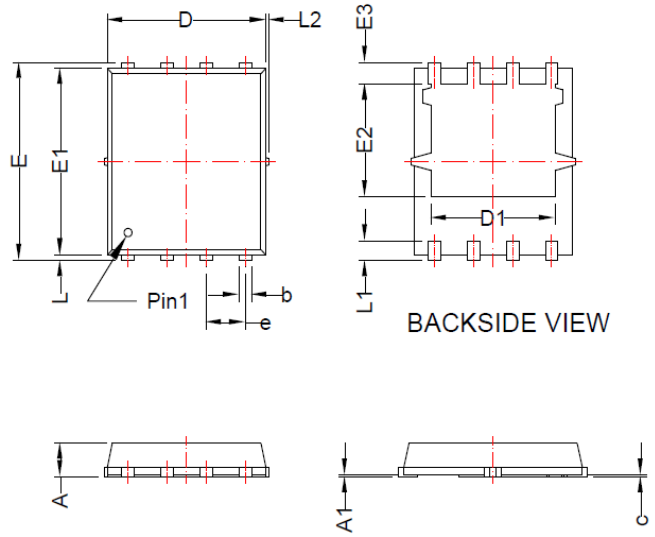
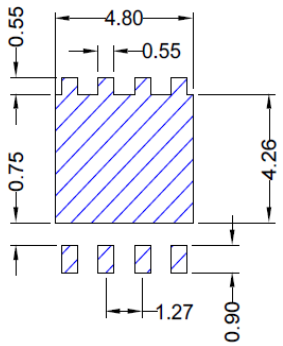


Recommended Land Pattern



Dimensions				
Symbol	**Millimeters**		**Inches**	
	Min	Max	Min	Max
A	0.80	1.20	0.031	0.047
A1	0.00	0.05	0.000	0.002
b	0.25	0.51	0.010	0.020
c	0.20	0.35	0.008	0.014
D	4.90	5.40	0.193	0.213
D1	3.40	4.60	0.134	0.181
E	5.90	6.20	0.232	0.244
E1	5.40	5.90	0.213	0.232
E2	3.20	3.80	0.126	0.150
E3	0.40	0.80	0.016	0.031
e	1.27BSC		0.050BSC	
L	0.10	0.25	0.004	0.010
L1	0.45	0.75	0.018	0.030
L2	-	0.15	-	0.006

NOTE:
DIMENSION D AND E1 DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL HOT EXCEED 0.5mm PER INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.5mm PER SIDE.

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