

LMN3612PJZF 30V N-Channel MOSFET

Features

- 30V, 5.3A, $R_{DS(ON)}$ =36m Ω @ V_{GS} =4.5V
- Improved dv/dt capability
- Fast switching
- Suit for 2.5V Gate Drive Applications
- Green Device Available
- SOT-23 package design

Product Description

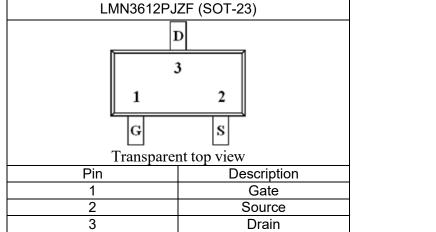
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

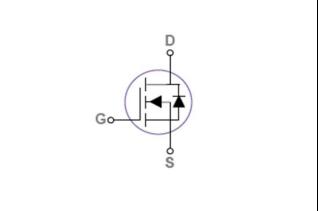
These devices are well suited for high efficiency fast switching applications.

Applications

- Notebook
- Load Switch
- LED applications

Pin Configuration







Ordering Information

Ordering Information						
Part Number	P/N	PKG code	ode Pb Free code Package		Quantity	
LMN3612PJZF	LMN3612P	JZ	F	SOT-23	3000 PCS	

Marking Information

Marking Information					
Part Marking	Part Number	LFC code			
PXWMM	Р	XWMM			

Absolute Maximum Ratings

(T_C=25°C Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V_{DSS}	Drain-Source Voltage	Drain-Source Voltage		V
V_{GSS}	Gate-Source Voltage	Gate-Source Voltage		V
I_	Continuous Drain Current	T _A =25°C	5.3	Α
I _D		T _C =100°C	3.4	^
I _{DM}	Pulsed Drain Current ²	Pulsed Drain Current ²		A
P_{D}	Power Dissipation (Tc=25°C	Power Dissipation (Tc=25°C)		W
	Power Dissipation (Derate a	bove 25°C)	0.012	v
TJ	Operating Junction Temperature		-55 to +150	°C
T _{STG}	Storage Temperature Range		-55 to +150	°C
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient ¹		80	°C/W

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Electrical Characteristics

(T_C=25°C Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
Static							
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I_D =1mA		0.06		V/°C	
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/ -250\	0.4	0.6	0.9	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{DS}=V_{GS}$, $I_D=250uA$		-3		mV/°C	
I _{GSS}	Gate Leakage Current	$V_{DS}=0V$, $V_{GS}=\pm 12V$			±100	nA	
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} =30V, V_{GS} =0V V_{DS} =24V, V_{GS} =0V, T_{J} =125°C			10	uA	
Is	Continuous Source Current	$V_G=V_D=0V$,			5.3	Α	
I _{SM}	Pulsed Source Current	Force Current			21.2	А	
$R_{DS(on)}$	Drain-Source On-Resistance	V_{GS} =4.5V, I_{D} =4A V_{GS} =2.5V, I_{D} =3A		31 36	36 45	mΩ	
G FS	Forward Transconductance	V _{DS} =10V, I _D =3A		7		S	
V_{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V			1	V	
Dynamic							
Q _g	Total Gate Charge ^{2,3})/ 40)/)/ 4.5)/		8.4	12		
Q _{gs}	Gate-Source Charge ^{2,3}	$V_{DS}=10V, V_{GS}=4.5V,$		1	2	nC	
Q_{gd}	Gate-Drain Charge ^{2,3}	I _D =4A		2.2	4		
C _{iss}	Input Capacitance	\/ -40\/ \/ -0\/		695	1000		
Coss	Output Capacitance	V_{DS} =10V, V_{GS} =0V, f=1MHz		45	65	pF	
C _{rss}	Reverse Transfer Capacitance	I= IIVIMZ		36	50		
t _{d(on)}	Turn-On Time ^{2,3}	V _{DD} =10V, I _D =1A,		4.5	9	ns	
t _r	Tum-On nine-,-			13	25		
t _{d(off)}	Turn-Off Time ^{2,3}	V_{GS} =4.5 V , R_{G} =25 Ω		27	51		
t _f	Tum-On Time-,-			8.3	16		
Rg	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz		1.5	3	Ω	

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width ≤300us , duty cycle ≤2%.
- 3. Essentially independent of operating temperature.



Typical Performance Characteristics

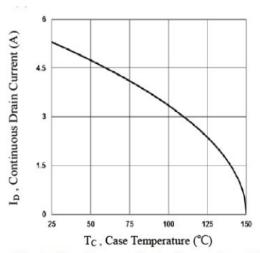


Fig.1 Continuous Drain Current vs. Tc

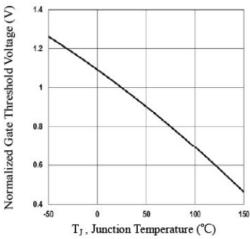


Fig.3 Normalized V_{th} vs. T_J

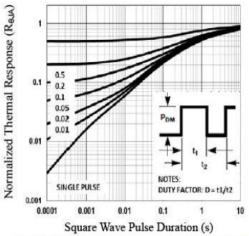


Fig.5 Normalized Transient Impedance

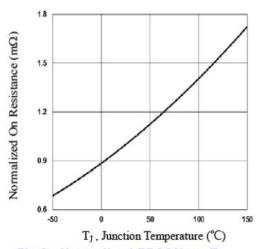


Fig.2 Normalized RDSON vs. T,

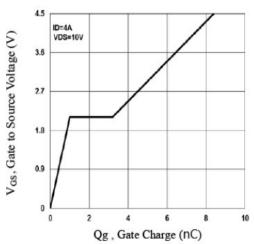


Fig.4 Gate Charge Waveform

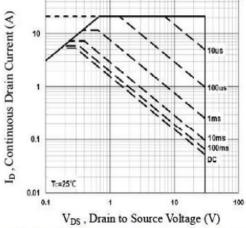
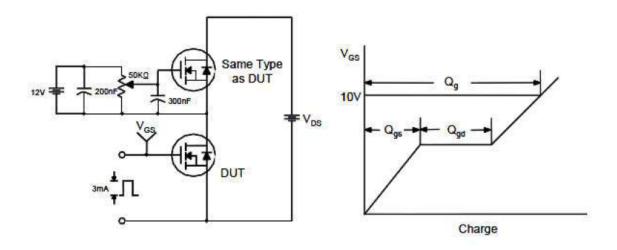


Fig.6 Maximum Safe Operation Area

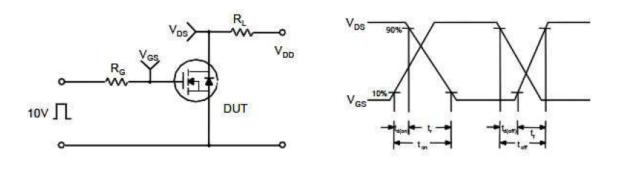


Typical Performance Characteristics(continue)

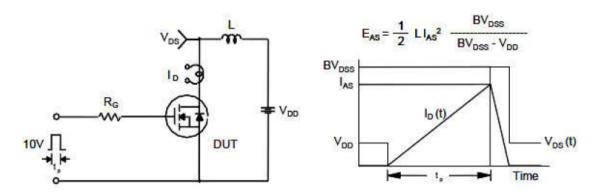
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

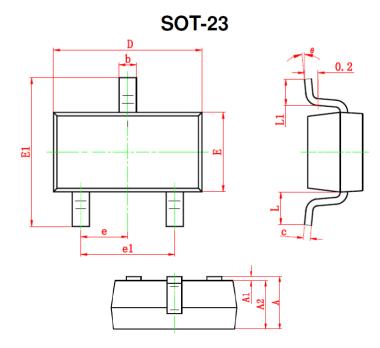


Unclamped Inductive Switching Test Circuit & Waveforms





Package Dimension:



	Dimensions				
Comple ed	Millimeters		Inches		
Symbol	Min	Max	Min	Max	
Α	0.90	1.20	0.035	0.043	
A1	0.00	0.10	0.000	0.004	
A2	0.90	1.10	0.035	0.039	
b	0.30	0.50	0.012	0.020	
С	0.08	0.15	0.003	0.006	
D	2.80	3.00	0.110	0.118	
E	1.20	1.40	0.047	0.055	
E1	2.25	2.55	0.089	0.10	
е	0.95 TYP		0.037 TYP		
e1	1.80	2.00	0.071	0.079	
L	0.55 REF		0.022 REF		
L1	0.30	0.50	0.012	0.020	
θ	0°	8°	0°	8°	



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