

LMN1073KX7F 20V N-Channel Enhancement Mode MOSFET

Features

- 20V/0.5A, R_{DS(ON)}=300mΩ@VGS=4.5V
- 20V/0.4A, R_{DS(ON)}=450mΩ@VGS=2.5V
- 20V/0.2A, R_{DS(ON)}=800mΩ@VGS=1.8V
- 20V/0.1A, R_{DS(ON)}=1200mΩ@VGS=1.5V
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation
- ESD Protected
- SOT-523 package design

Product Description

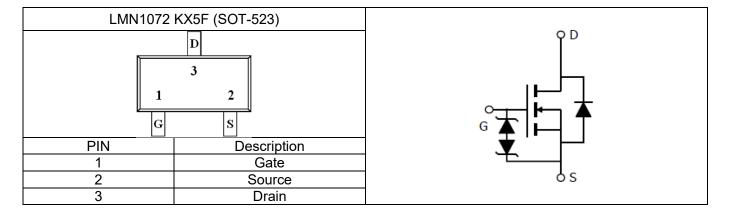
LMN1072 KX7F, N-Channel enhancement mode MOSFET, uses Advanced Trench

Pin Configuration

Technology to provide excellent RDS(ON), low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer, and low in-line power loss are needed in commercial industrial surface mount applications.

Applications

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Smart Phones, Pagers





Ordering Information

Ordering Information					
Part Number	P/N	PKG code	Pb Free code	Package	Quantity
LMN1072KX7F	LMN1072K	X7	F	SOT-523	3000 PCS

Marking Information

Marking Information					
Part Marking	Part Number	LFC code			
2W	2	WM			

Absolute Maximum Ratings

(T_C=25°C Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V _{DSS}	Drain-Source Voltage	Drain-Source Voltage		V
V _{GSS}	Gate-Source Voltage	Gate-Source Voltage		V
l _D	Continuous Drain Current	T _A =25°C	0.7	Α
		T _A =70°C	0.5	A
I _{DM}	Pulsed Drain Current	Pulsed Drain Current		А
ls	Continuous Source Curre	Continuous Source Current (Diode Conduction)		А
P _D	Power Dissipation	_A =25°C	0.27	W
		_A =70°C	0.18	VV
TJ	Operating Junction Temp	Operating Junction Temperature		°C
T _{STG}	Storage Temperature Ra	Storage Temperature Range		°C
R _{0JA}	Thermal Resistance-Junction to Ambient		463	°C/W



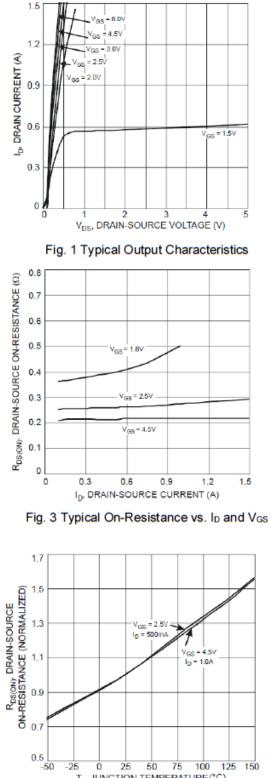
Electrical Characteristics

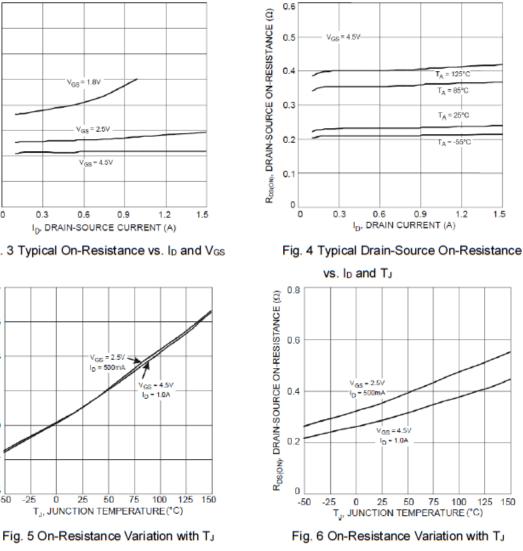
(T_C=25°C Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
Static							
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20			- V	
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , ID=250uA	0.3		1	v	
I _{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 10V$			±10	uA	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V			1		
		V _{DS} =16V, V _{GS} =0V T _J =85°C			30	uA	
		V _{GS} =4.5V, I _D =0.5A		210	300	mΩ	
П	Drain Source On Desistance	V _{GS} =2.5V, I _D =0.4A		285	450		
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =1.8V, I _D =0.2A		430	800		
		V _{GS} =1.5V, I _D =0.1A		710	1200		
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =0.4A			1.2	S	
V_{SD}	Diode Forward Voltage	I _S =0.5A, V _{GS} =0V			1.3	V	
		Dynamic					
Q_{g}	Total Gate Charge	(-10)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1		0.73		nC	
Q _{gs}	Gate-Source Charge	V _{DS} =10V, V _{GS} =4.5V, I _D =0.25A		0.93			
Q_gd	Gate-Drain Charge	ID-0.23A		0.12			
Ciss	Input Capacitance			60.7			
Coss	Output Capacitance	V_{DS} =16V, V_{GS} =0V,		9.7		pF	
Crss	Reverse Transfer Capacitance	f=1MHz		5.4		-	
t _{d(on)}	Turn On Time)/ _40)/ D _470		5.1			
tr	- Turn-On Time	V_{DD} =10V, R _L =47Ω,		7.4		ns	
$t_{d(off)}$	Turn Off Time	l _D =0.2A, V _{GS} =4.5V, R _G =10Ω		26.7			
t _f	- Turn-Off Time	KG-1022		12.3			



Typical Performance Characteristics





1.5

1.2

0.9

0.6

0.3

0

0

0.5

Ib, DRAIN CURRENT (A)

109

TA 150°0

25*(

2

2.5

3

1.5

Fig. 2 Typical Transfer Characteristics

Vos, GATE SOURCE VOLTAGE (V)

25

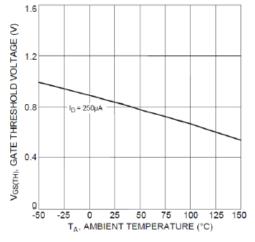
-25 Ó 50

T_J, JUNCTION TEMPERATURE (°C)

100



Typical Performance Characteristics(continue)





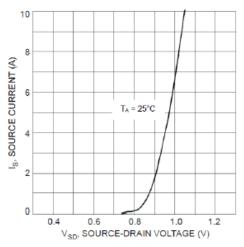


Fig. 8 Diode Forward Voltage vs. Current

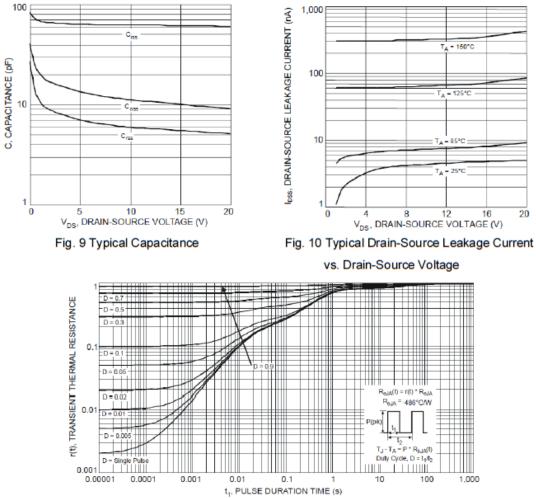
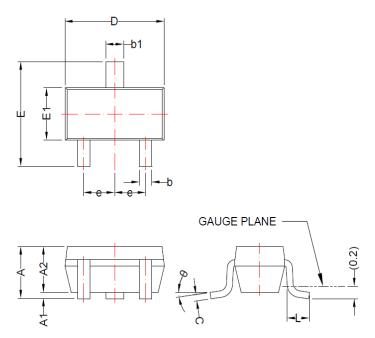


Fig. 11 Transient Thermal Response



SOT-523



	Dimensions					
Symbol	Millimeters		Inches			
Symbol	Min	Max	Min	Max		
Α	0.60	0.95	0.024	0.037		
A1	0.00	0.10	0.000	0.004		
A2	0.60	0.85	0.024	0.033		
b	0.15	0.30	0.006	0.012		
b1	0.25	0.40	0.010	0.016		
С	0.08	0.25	0.003	0.010		
D	1.40	1.80	0.055	0.071		
E	1.40	1.80	0.055	0.071		
E1	0.70	0.90	0.028	0.035		
е	0.50BSC		0.020)BSC		
L	0.26	0.46	0.010	0.018		
θ	0°	8°	0°	8°		



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