

LMP3131SF 30V P-Channel MOSFET

Features

- -30V/-5.7A, $R_{DS(ON)} < 32m\Omega @ V_{GS} = -10V$
- Fast switching
- Suit for -4.5V Gate Drive Applications
- Green Device Available
- SOP-8 package design

Product Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance,

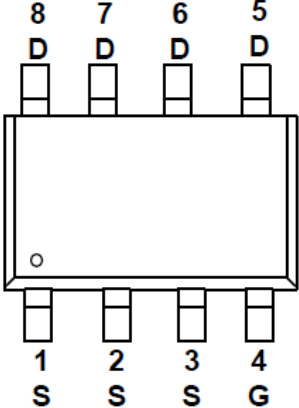
provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

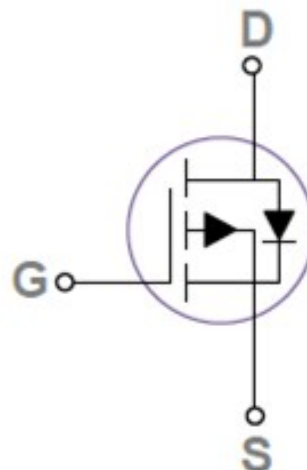
These devices are well suited for high efficiency fast switching applications.

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Pin Configuration

LMP3131SF (SOP-8)	
	
PIN	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain



Ordering Information

Ordering Information					
Part Number	P/N	PKG code	Pb Free code	Package	Quantity
LMP3131SF	LMP3131	S	F	SOP-8	4000

Marking Information

Marking Information		
Part Marking	Part Number	LFC code
3131S XWMMMM	3131S	XWMMMM

Absolute Maximum Ratings

(T_C=25°C Unless otherwise noted)

Symbol	Parameter	Typical	Unit
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	T _A =25°C	A
		T _A =70°C	
I _{DM}	Pulsed Drain Current ¹	-22.8	A
EAS	Single Pulse Avalanche Energy ²	39.2	mJ
IAS	Single Pulse Avalanche Current ²	-28	A
P _D	Power Dissipation (T _A =25°C)	1.47	W
T _J	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance-Junction to Ambient	85	°C/W

Note:

1. Repetitive Rating: Pulsed width limited by maximum junction temperature.

Electrical Characteristics

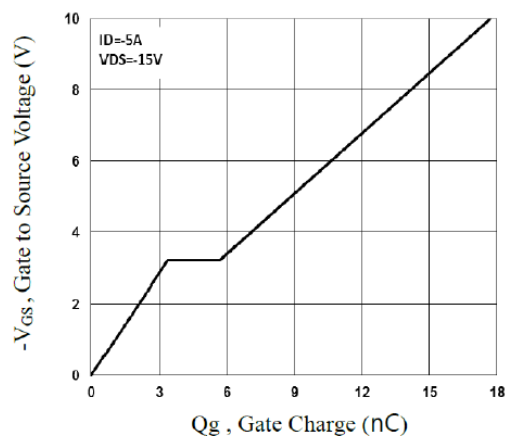
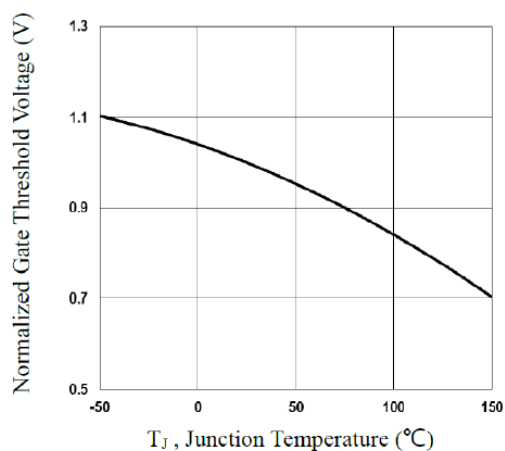
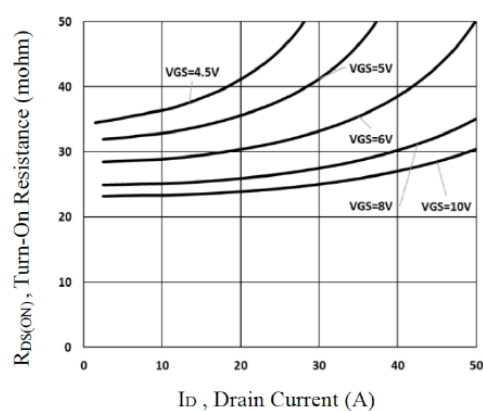
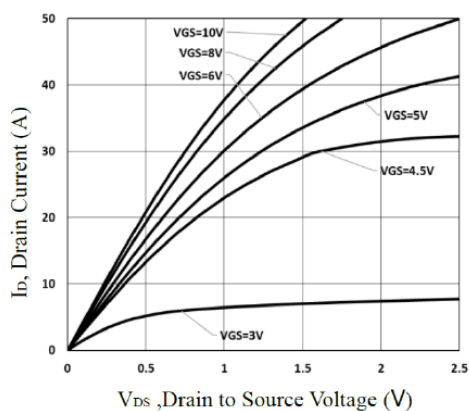
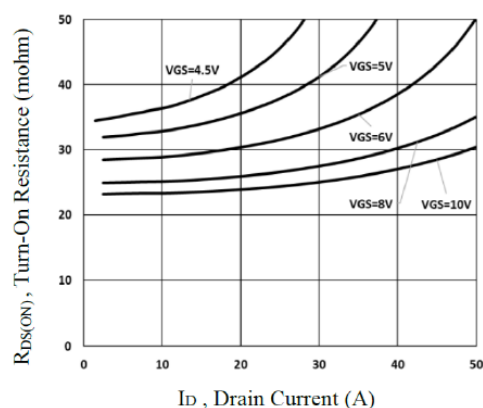
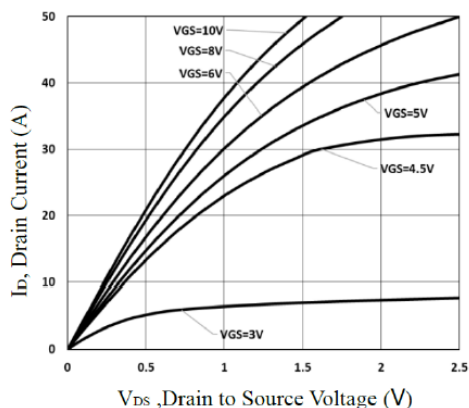
(T_C=25°C Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	-30			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	-1.3	-1.7	-2.3	V
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-30V, V _{GS} =0V T _J =25°C			-1	uA
		V _{DS} =-24V, V _{GS} =0V, T _J =125°C			-10	
I _S	Continuous Source Current	V _G =V _D =0V, Force Current			-5.5	A
I _{SM}	Pulsed Source Current				-11	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =-4A		26	32	mΩ
		V _{GS} =4.5V, I _D =-3A,		42	46	
g _{FS}	Forward Transconductance	V _{DS} =-10V, I _D =-3A		5		S
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-1A			-1	V
Dynamic						
Q _g	Total Gate Charge ^{2,3}	V _{DS} =-15V, V _{GS} =-4.5V, I _D =-5A		8	15	nC
Q _{gs}	Gate-Source Charge ^{2,3}			3.3	6	
Q _{gd}	Gate-Drain Charge ^{2,3}			2.3	5	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz		757	1280	pF
C _{oss}	Output Capacitance			122	210	
C _{rss}	Reverse Transfer Capacitance			88	175	
t _{d(on)}	Turn-On Time ^{2,3}	V _{DD} =15V, I _D =-1A, V _{GS} =-10V, R _G =6Ω		4.6	9	ns
t _r	Rise Time ^{2,3}			14	26	
t _{d(off)}	Turn-Off Time ^{2,3}			34	58	
t _f	Fall Time ^{2,3}			18	35	

Note :

- The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
- Essentially independent of operating temperature.

Typical Performance Characteristics



Typical Performance Characteristics(continue)

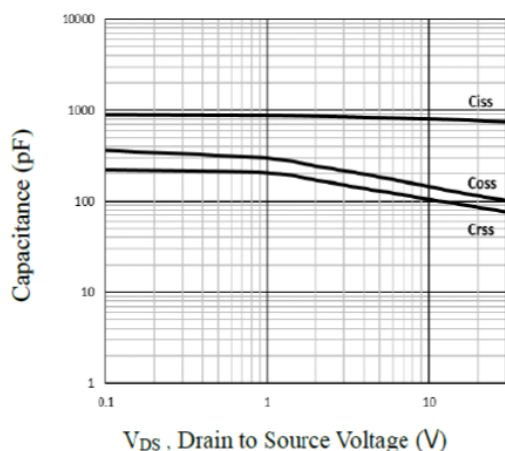


Fig.7 Capacitance Characteristics

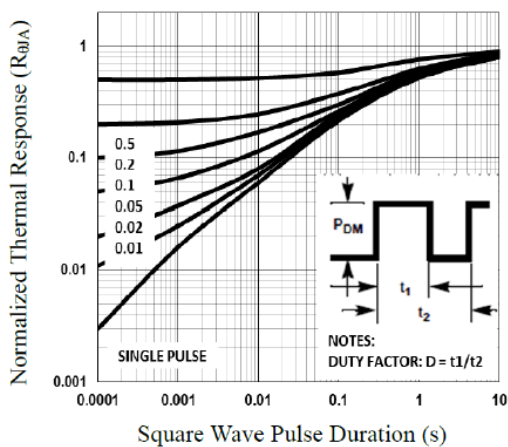


Fig.8 Normalized Transient Impedance

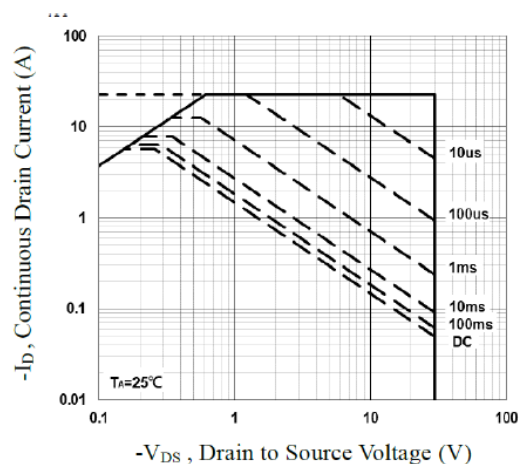


Fig.9 Maximum Safe Operation Area

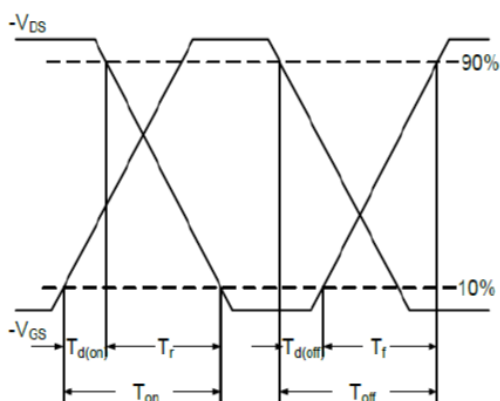


Fig.10 Switching Time Waveform

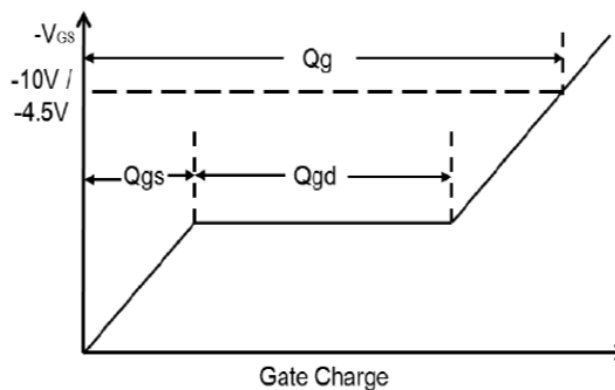
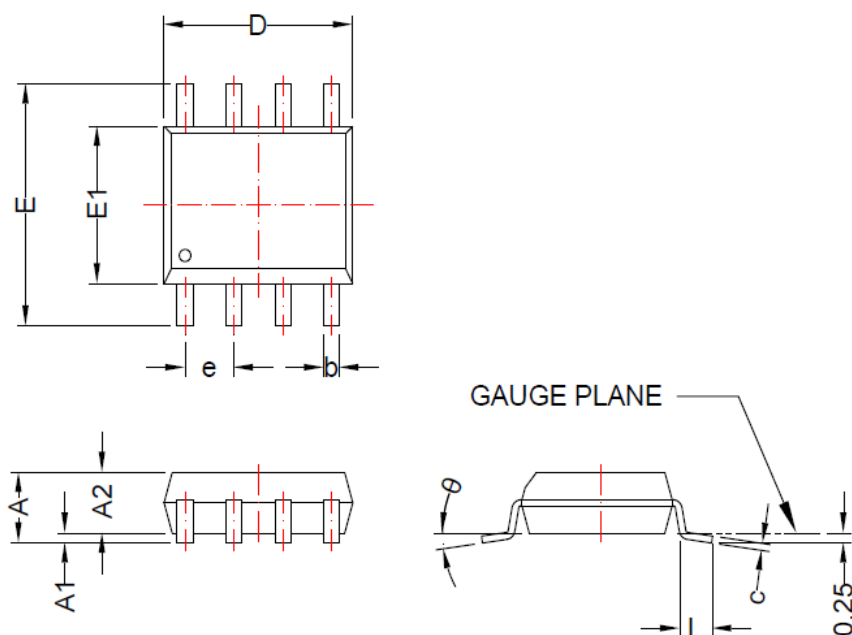


Fig.11 Gate Charge Waveform

Package Dimension:

SOP-8



DIMENSION D DOES NOT INCLUDE MOLD FLASH,PROTRUSIONS OR GATE BURRS.MOLD FLASH,PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.25mm PER INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25mm PER SIDE.

Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	---	1.75	---	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	---	0.049	---
b	0.31	0.51	0.012	0.020
c	0.10	0.25	0.004	0.010
D	4.70	5.10	0.185	0.201
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
L	0.4	1.27	0.016	0.050
θ	0°	8°	0°	8°

NOTICE:

LFC Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all LFC Semiconductor products described or contained herein. LFC Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. LFC Semiconductor makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Information furnished is believed to be accurate and reliable. However LFC Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of LFC Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of LFC Semiconductor.